

ABSTRACT OF THE DISCLOSURE

An interleaver memory access apparatus of a CDMA system includes: an interleaver memory for storing code symbols to be transmitted; a shift register unit
5 for simultaneously receiving 18 bit code symbols from the interleaver memory and outputting it by 6 code symbols; an index decoding unit for decoding the 6 code symbols outputted from the shift register unit and generates a Walsh index; an address generator and control logic for controlling the access operation of the interleaver memory and the input and output operation of the shift register unit and
10 the index coding unit; and an orthogonal modulator for outputting 64 Walsh codes on the basis of the Walsh index outputted from the index decoding unit. Since the interleaver memory performs reading operation by only using the row address, a simple addressing is implemented for the memory access, so that the performance of the overall CDMA system as well as the data access speed can be
15 enhanced.